



PRODUCT SPECIFICATION

(√) PRODUCT INFORMATION

() APPROVAL SPECIFICATION

Any modification of specification is not allowed without Samsung's permission.

CUSTOMER	CNC	MODEL	LSM270DP01
PROGRAM	_	EXTENSION CODE	-

CUSTOMER APPROVAL & FEEDBACK	

ARPPROVED BY	13 / Mar. / '17	Scott Lim	Scott Lim
PREPARED BY	13 / Mar. / '17	Sejin Park	Sejin Park
	LCD CS Samsung Dis	E Group play Co., Lto	J .

Product Configuration Approval Sheet

Description

Items	Content	
Customer	CNC	
Product Name	LSM270DP01	
Project Name	-	

Customer System Configuration

Items		Content
Syst	em Name	
Р	Purpose	-
IC	Scalar	
	LED Driver	0
Inpu	it Interface	_
OS (AIO)		_
Graph	ic Card (AIO)	_

Notice : SDC product approval spec guarantee a above customer system.

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Revision History

Version	Date	Page		Description	
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1. General Description

Overview

LSM270DP01 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit. The resolution of a 27" is 2560 x 1440 (QHD) and this model can display up to 16.7 million colors.

Features

Application - Workstation & Desktop monitors - Display terminals for AV Products - Monitors for Industrial machine	
DE (Data Enable) only mode	
eDP Interface	
RoHS, Halogen Free	

General Information

Items	Specification	Unit
Pixel Pitch	0.2331 (W) x 0. 2331 (H)	mm
Active Display Area	596.736(H) x 335.664(V)	mm
Surface Treatment	Anti-Glare Type, Haze 25%	-
Display Colors	16.7M	colors
Number of Pixels	2,560 x 1,440	pixel
Pixel Arrangement	RGB vertical stripe	-
Display Mode	Normally Black	-
Power Consumption	Panel 4.8 (Typ. @ 60Hz, Mosaic) Panel 5.9 (Typ. @ 144Hz, Mosaic)	W

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Mechanical Information

	Item	Min.	Тур.	Max.	Unit	Note
	Horizontal (H)	608.70	609.20	609.70	mm	-
		347.92	348.42	348.92	mm	Without PCB
Module size	Vertical (V)	415.30	415.80	416.30	mm	With PCB
		1.055	1.155	1.255	mm	Without PCB
	Depth (D)	-	3.05	-	mm	With PCB
	Weight	-	-	600	g	

Note (1) Mechanical tolerance is \pm 0.5mm unless there is a special comment.

2. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V _{DD}	-0.3	14	V	(1)
Operating Temperature	T _{OPR}	0	50	Ĉ	(2)
Storage temperature	T _{stg}	-20	60	Ĉ	(2)
Glass surface temperature (Operation)	T _{SUF}	0	65	Ĉ	(3)

Note (1) Ta= 25 ± 2 °C

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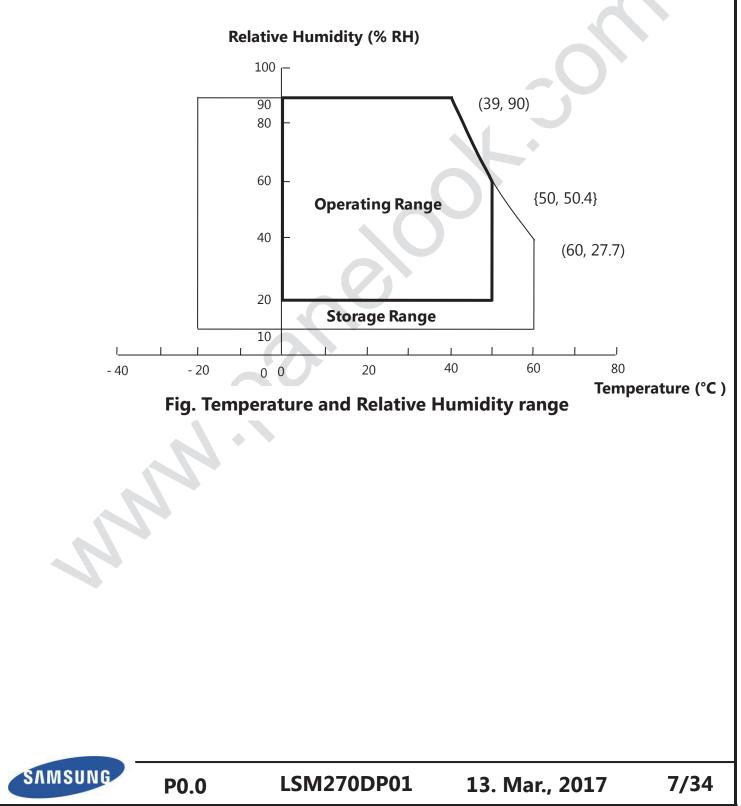
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- (2) Temperature and relative humidity range are shown in the figure below. a. 90 % RH Max. (Ta \leq 39 °C)
 - b. Maximum wet-bulb temperature at 39 °C or less. (Ta \leq 39 °C)
 - c. No condensation.
- (3) The maximum operating temperature of LCD module is defined with surface temperature of active area. Under any condition, the maximum ambient operating temperature should be keeping the surface of active area not any higher than 65 °C



3. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent. Measuring equipment : SR-3, RD-80S (TOPCON), EZ-Contrast (Eldim)

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	winte	Wv'		-	0.483	-		
	White	Wu'		_	0.176	-		
	Blue	Bv'		-	0.264	-		
(CIE 1976)	Plus	Bu'		-	0.127	-		
Color Chromaticity	Green	Gv'		_	0.558	-		
	Charter	Gu'		-	0.115	_		
	Red	Rv'		-	0.526	-		
	Dert	Ru'		-	0.471	-		SR-3
	White	Wy	Angle		0.363			(7),(8)
		Wx	Viewing		0.297			
	Blue	Ву	θ _{υ,D} =0		0.124			
(CIE 1931)	5	Bx	Normal θ _{L,R} =0	- 0.030	0.134	+0.030		
Color Chromaticity	Green	Gy	Normal		0.592	0.000		
		Gx	•		0.275			
	Red	Ry			0.328			
		Rx			0.662			
Brightness Unit (9 Points)	-	B _{uni}		-	-	25	%	(4) SR-3
Transmittar	nce	-		-	2.28	-	%	(6) SR-3
Response T (w/ DCC)		GtoG		-	12	-	msec	(5) RD-80
Contrast Ra (Center of sci		C/R		2100	3000	-	-	(3) SR-3
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note

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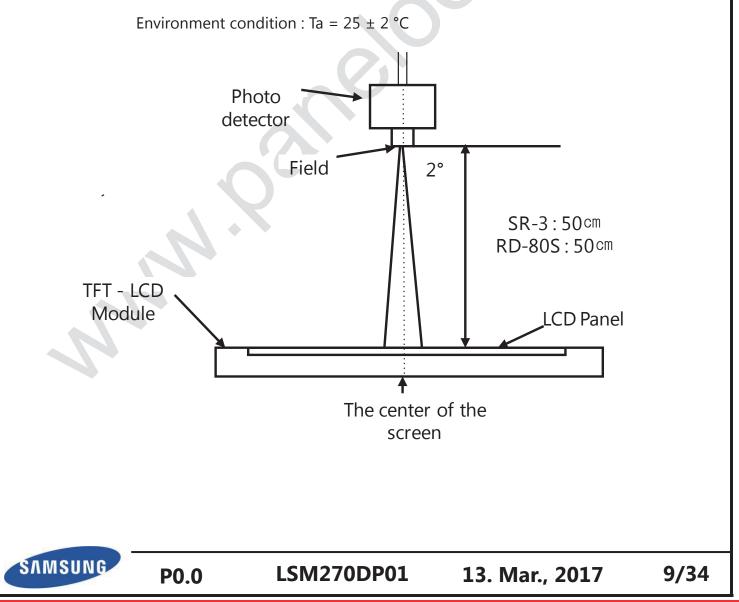
Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note	
	llor	θ		80	89	-			
Viewing	Hor.	θ _R	- CR≥10	CD> 10	80	89	-	Desmost	(8)
Angle		θυ		80	89	-	Degrees	EZ- Contrast	
	Ver.	θ		80	89	-			

* Optical specification is valid only standard optical source (Estimation of Image Quality Group

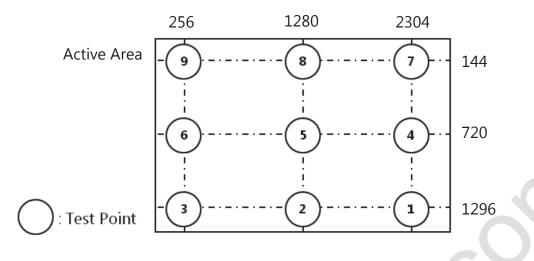
- * Light Source : D65 Standard light
- * Guaranteed only in terms of the cell condition
- * Only Flat Status assurance

Note (1) Test Equipment Setup

The measurement should be executed in a stable, windless and dark room between 30min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.



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- (3) Definition of Contrast Ratio (CR)
 - : Ratio of gray max (G_{max}) & gray min (G_{min}) at the center point (5) of the panel

$$CR = \frac{G_{max}}{G_{min}}$$

 G_{max} : Luminance with all pixels white G_{min} : Luminance with all pixels black

(4) Definition of 9 points brightness uniformity

$$B_{uni} = 100 \ x \ \frac{B_{max} - B_{min}}{B_{max}}$$

B_{max} : Maximum brightness *B_{min}* : Minimum brightness

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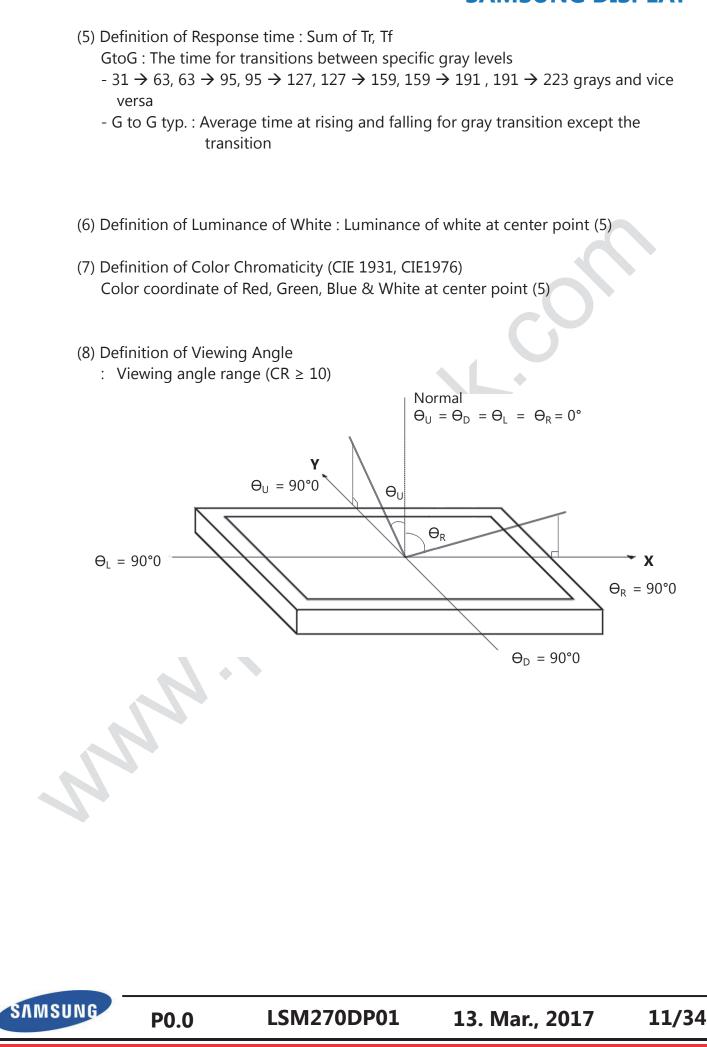
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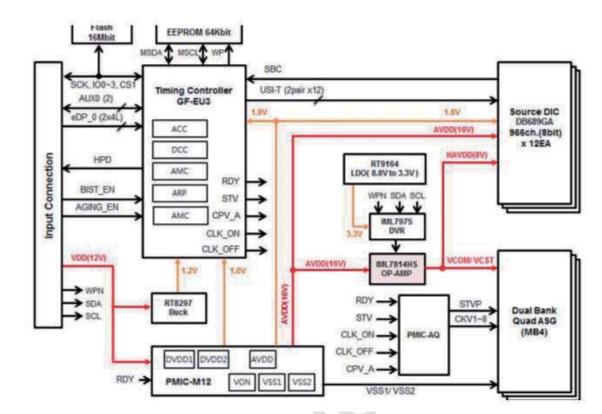


Fig. Function Block Diagram

Note (1) The connector for display data & timing signal should be connected

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5. Electrical Characteristics

5.1 TFT LCD Module

The connector for display data & timing signal should be connected.

 $Ta=25 \pm 2^{\circ}C$

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I	Symbol	Min.	Тур.	Max.	Unit	Note	
Voltage of	Power Supply	V _{DD}	10.8	12.0	13.2	V	(1)
Current of	White		-	400	460	mA	
Power Supply	Black	I_{DD}	-	395	455	mA	(2),(3)
(@60Hz)	Mosaic		-	395	455	mA	
Current of Power	White		_	500	575	mA	
Supply	Black	$I_{_{DD}}$	-	490	565	mA	(2),(4)
(@144Hz)	Mosaic		-	490	565	mA	
	Power Consumption(@60Hz) [Mosaic pattern]			4.8	-	Watt	(3)
Power Consu [Mosai	P _{LCD_144Hz}	-	5.9	-	Watt	(4)	
Rush	Current	I _{RUSH}	-	-	2.5	А	(6)

Note (1) The ripple voltage should be controlled under 10% of V_{DD}

- (2) Definition of V_{DD} Power Dip
 - The above conditions are for the glitch of the input voltage.
 - For stable operation of an LCD Module power, please follow them.

80% .

GND ·

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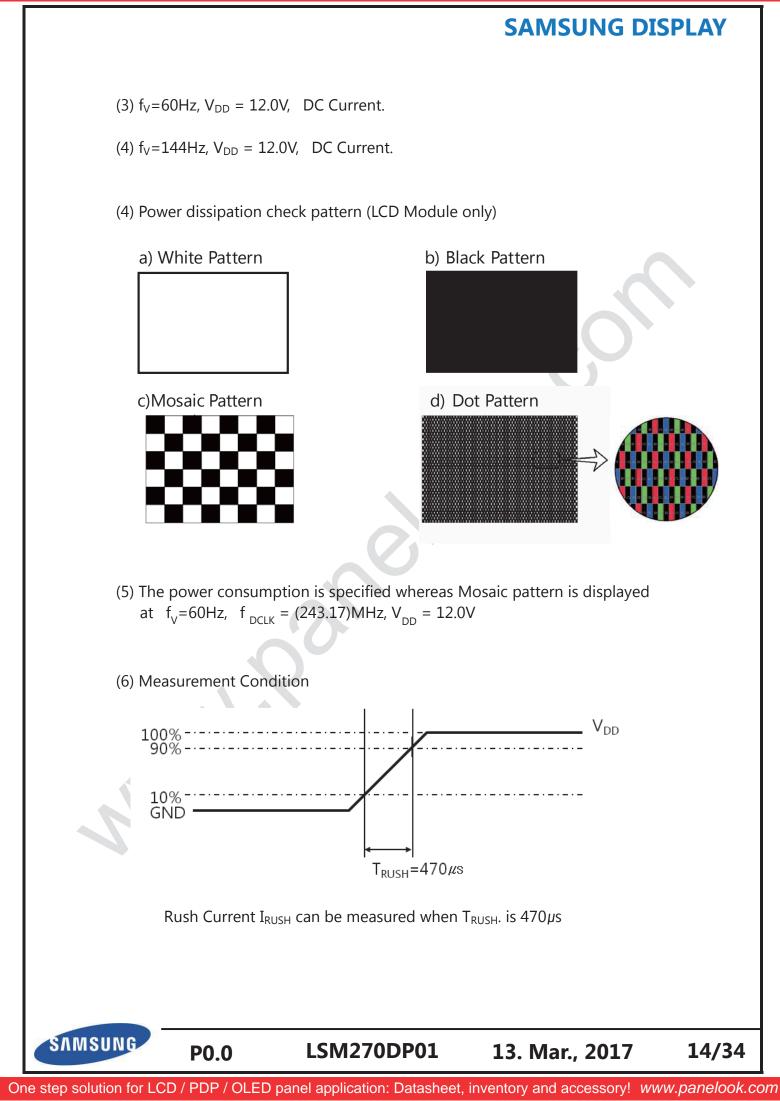
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$$\begin{split} 10.8 \mathsf{V} &\leq \mathsf{V}_{\mathsf{DD}} \leq 13.2 \mathsf{V} \\ \text{If } \mathsf{V}_{\mathsf{DD}}(\mathsf{Typ.}) \ge 80\% \leq \mathsf{V}_{\mathsf{CC}} \leq \mathsf{V}_{\mathsf{DD}}(\mathsf{Typ.}) \ge 90\%, \\ \text{then } 0 < \mathsf{Td} \le 20 \text{msec} \end{split}$$

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 V_{CC}

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5.2 eDP Characteristics

5.2.1. eDP Input Characteristics

 $Ta=25 \pm 2^{\circ}C$

Symbol	Parameter	Min	Тур	Max	Unit		
		75	100	130			
		150	200	250			
V	Output differential voltage V/ P	250	300	350	m\/		
V_{od}	Output differential voltage $ V_{top} - B_{bot} _1$	320	400	480	mV		
		400	500	600			
		500	600	700	l		
V_{cm}	Output common mode voltage (V_{top} + B_{bot})/2	0.4	0.55	0.65	V		
$ riangle V_{od}$	Variation in V_{od} between 0 and 1	-	-	30	mV		
$ riangle V_{cm}$	Variation in V_{cm} between 0 and 1		-	30	mV		
		70	100	130			
		84	120	156			
		91	130	169			
		98	140	182			
		119	170	221			
R_{tx}	Transmitter differential output impedance 2	129.5	185	240.5	ohm		
		140	200	260			
		154	220	286			
		224	320	416			
		252	360	468			
		308	440	572			

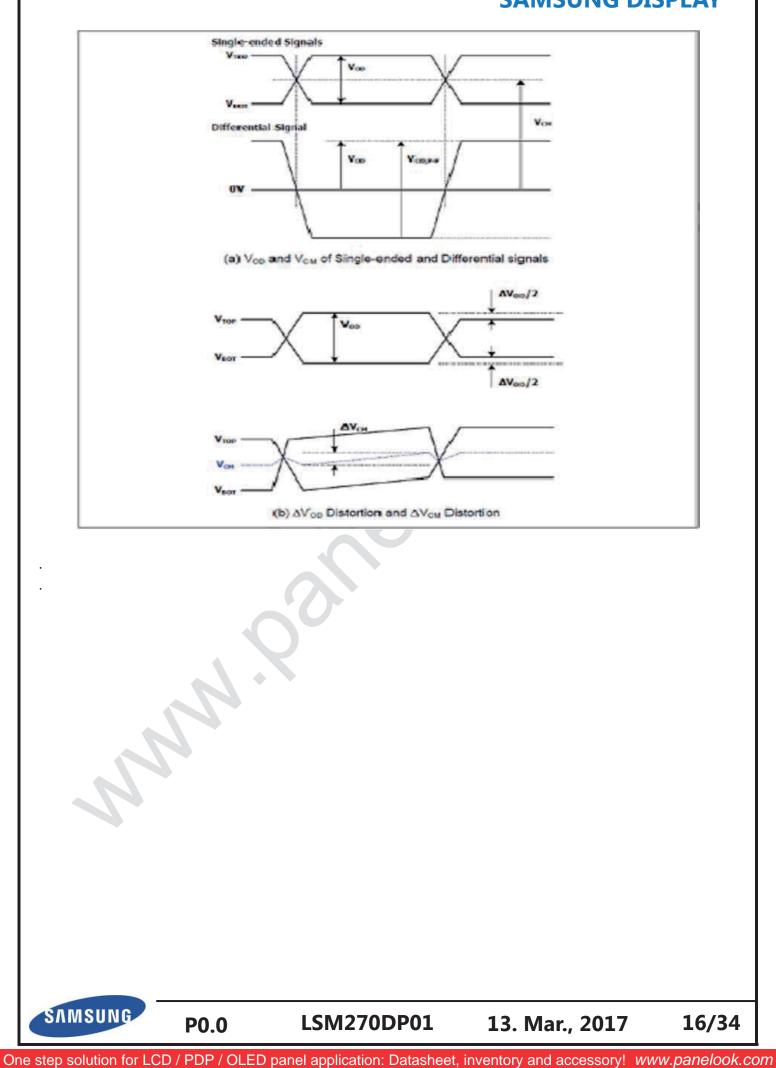
Note (1) |V_{top} – B_{bot}| : To reduce power consumption and peak current, Vod is programmed By an external EEPROM

(2) R_{tx} is programmed by an external EEPROM, This is for EMI reduction by controlling rising falling time of output signals.

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5.3 Interface Timing Specification

5.3.1. Timing Parameters

SIGNAL	Min.	Тур.	Max.	Note
H active	-	2560	-	Pixel
H blank	120	160	200	Pixel
H Total	2665	2720	2760	Pixel
V active	-	1440	-	Line
V blank	30	50	2656	Line
V total	1470	1490	4627	Line
Horizontal refresh rate	70.56	89.4	214.56	KHz
Vertical refresh rate	48	60	144	Hz
Pixel clock frequency	189.1	243.17	595	MHz

Note (1) DE only mode

- While operation, DE signal should be have the same cycle.
- (2) Best operation clock frequency is 243.17 MHz (60Hz)
- (3) Clock frequency = Frame frequency x T_V (Typ.) x T_H (Typ.)
- (4) Max, Min variation range is at main clock typical value 243.17MHz
- (5) Except Best operation clock frequency range, FOS (Flicker & Brightness & Crosstalk etc.) are not guaranteed

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SAMSUNG DISPLAY 5.3.2. Timing diagrams of interface signal (DE only mode) T_V T_{VD} T_{VB} DE T_H T_{HD} DE n n n n $\mathsf{D}_{\mathsf{CLK}}$ T_C DATA SIGNALS T_{C} T_{CL} Т_{СН} $\mathsf{D}_{\mathsf{CLK}}$ $0.5 \, V_{CC}$ T_{DH} T_{DS} DISPLAY • 0.5 V_{CC} DATA T_{ES} DE ·· 0.5 V_{CC} SAMSUNG P0.0 LSM270DP01 13. Mar., 2017 18/34

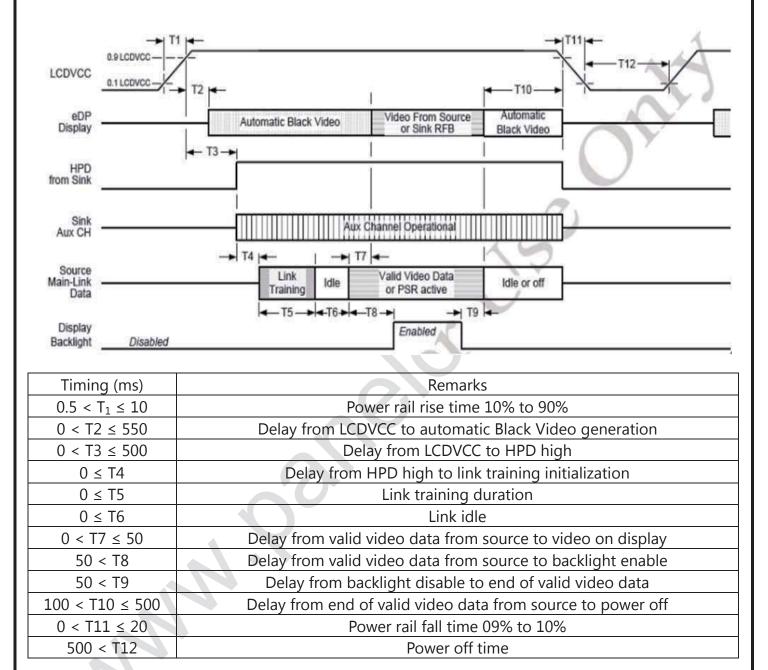
5.4 Input Signals, Basic Display Colors and Gray Scale of Each Color

										1		DA	ATA S	SIGN	۹L											GRAY
COLOR	DISPLAY (8bit)				RI	r –					1			EEN							1	UE				SCALE LEVEI
	BLACK	R0 0	R1 0	R2 0	R3 0	R4 0	R5 0	R6 0	R7 0	G0 0	G1 0	G2 0	G3 0	G4 0	G5 0	G6 0	G7 0	В0 0	B1 0	B2 0	B3 0	B4 0	B5 0	В6 0	В7 0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	CYAN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
BASIC COLOR	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	MAGENTA	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0
	DEACK	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2
GRAY	DARK ↑	0	1	0	0	0	0	0	0	0	0	0	0	0	U	0	0	0	0	0	0	0	0	0	0	ΓZ
SCALE OF RED	Ļ	:	:	:	:	:	:			:	:	:	:	:				:	:	:	:	:	:			•
	LIGHT	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R253
		0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R254
	RED	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R25
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0
		0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G1
	DARK	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G2
GRAY SCALE OF GREEN	Î	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
	↓ LIGHT	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G25
	LIGHT	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G25
	GREEN	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	G25
	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	BO
	DEACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	B0 B1
	DARK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	B1 B2
GRAY SCALE	1	:	:	:	:	:	:			:	:	:	:	:	:			:	:	:	:	:	:			
OF BLUE	ļ	L																								
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	B253
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	B254
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	B25
Note	e (1) Definiti - Rn : F Input	Red	Gr	ay,	G					-					-			-		el)						
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5.5 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



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Note (1) The power voltage from system shall be supplied to the input pin of LCD constantly.

(2) The Sink must include the ability to automatically generate black video autonomously. The Sink must automatically enable black video under the following conditions: Upon LCDVCC power-on (within T2 max)

When the "NoVidoeStream_Flag" is received from the Source (at the end of T9) When the no main link data, is received from the Source. Black Video must be display within 50ms (max) from the start of either condition.

- (3) The Sink may implement the ability to disable the automatic black video function, as described in Note 1, above, for system development and debugging purposes.
- (4) The Sink must support AUX Channel polling by the source immediately following LCDVCC power-on without causing damage to the Sink device (the Source can re-try if the Sink is not ready.) The Sink must be able to respond to an AUX Channel transaction with the time specified within T3 max

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5.6 Input Terminal Pin Assignment

5.6.1. Input signal Pin Assignment

Connector : KN35-51S-0.5H (HRSK) eDP 51Pin CNT

Pin No.	Symbol	Function
1	PVDD	Power Supply +12V
2	PVDD	Power Supply +12V
3	PVDD	Power Supply +12V
4	PVDD	Power Supply +12V
5	PVDD	Power Supply +12V
6	PVDD	Power Supply +12V
7	N.C	N.C
8	N.C	N.C
9	AGING_EN	Reserved for LCD manufacturer's use
10	N.C	N.C
11	BIST_EN	Reserved for LCD manufacturer's use
12	GND	Ground
13	WPN	Reserved for LCD manufacturer's use (WPN)
14	SCL	Reserved for LCD manufacturer's use (SCL)
15	SDA	Reserved for LCD manufacturer's use (SDA)
16	GND	Ground
17	GND	Ground
18	DRX0P	Signal Sink Lane
19	DRX0N	Signal Sink Lane
20	GND	Ground
21	DRX1P	Signal Sink Lane
22	DRX1N	Signal Sink Lane
23	GND	Ground
24	DRX2P	Signal Sink Lane
25	DRX2N	Signal Sink Lane
26	GND	Ground
27	DRX3P	Signal Sink Lane
28	DRX3N	Signal Sink Lane
29	GND	Ground
30	DAUXN	Auxiliary Channel

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Pin No.	Symbol	Function
31	DAUXP	Auxiliary Channel
32	GND	Ground
33	HPD	Hot Plug Detect
34	GND	Ground
35	N.C	N.C
36	GND	Ground
37	N.C	N.C
38	N.C	N.C
39	GND	Ground
40	N.C	N.C
41	N.C	N.C
42	GND	Ground
43	N.C	N.C
44	N.C	N.C
45	GND	Ground
46	N.C	N.C
47	N.C	N.C
48	GND	Ground
49	N.C	N.C
50	N.C	N.C
51	GND	Ground

Note (1) Pin number starts from Left side

- (2) All GND pins should be connected together and also be connected to the LCD's metal chassis.
- (3) All power input pins should be connected together.
- (4) All NC pins should be separated from other signal or power

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5.7 IC Tc Spec

Temp	erature Spec	D-IC	T-con	PMIC
	Silk	-	IC1	IC2
	Tc	125℃	100°C	122°C

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6. Outline Dimension

[Refer to the next page]

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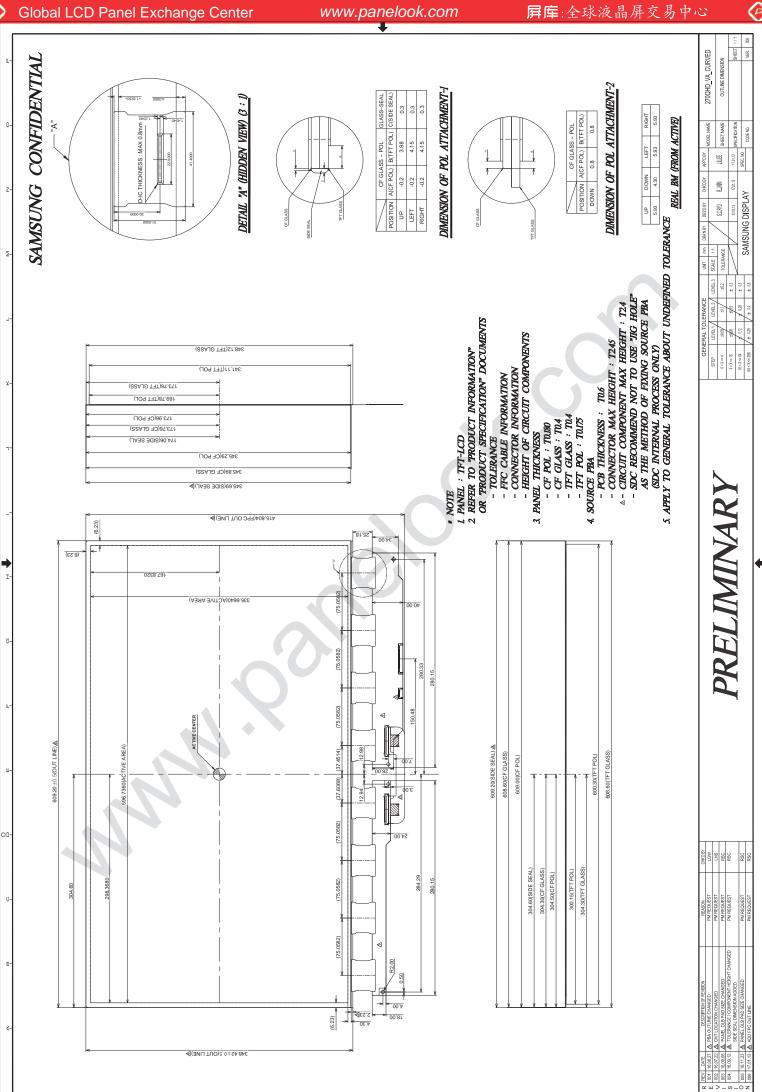
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Global LCD Panel Exchange Center

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7. Reliability test

Tes	t Item	Condition	Time/Cycle	
HT	CL	50°C , Bias	500 hrs	
LTC	DL	0°C , Bias	500 hrs	
TH	В	40°C / 90% , Bias	500 hrs	
HTS	S	70°C , No Bias	500 hrs	
LTS		-20°C , No Bias	500 hrs	
The	ermal Cycle	-20°C/30min ~ +60°C/30min , No bias	100 cycle	
ESD	Non-Operation Open Cell	Air(non-contact) : 150pF, 330Ω, 100point	± 15kV	
Altit	tude	Thermal :-10~50℃, 15000ft(Operating), 40000ft(Non-operating) Normal :45℃, 15000ft	8Hr 10Hr	

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these should be no change which may affect practical display functions.

- HTOL / LTOL : High/Low Temperature Operating Life -
- : Temperature Humidity Bias THB

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HTS/LTS : High/Low Temperature Storage

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LSM270DP01

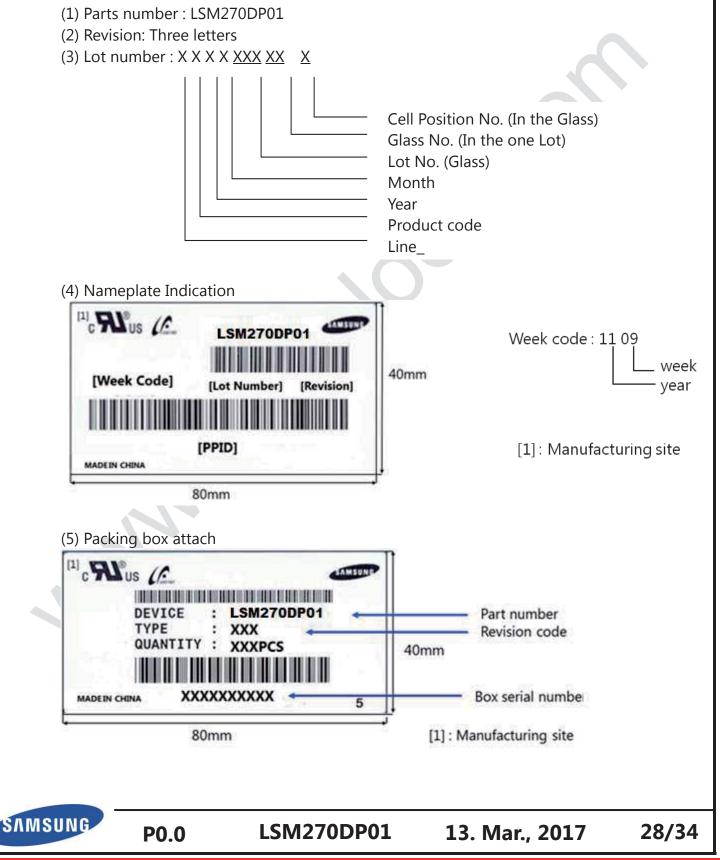
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8. Packing

8.1 Marking

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.



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9. General Precautions

9.1 Handling Precautions

- A. When assembling LCD module into its system, using all the mounting holes is strongly suggested.
- B. Keep LCD module from any external shock or force which can cause physical damage to LCD module. It may cause improper operation or damage to LCD module.
- C. Polarizer films are very fragile. It could be damaged easily. Do not press or scratch the surface harder than a HB pencil lead.
- D. Wipe off water droplets or oil immediately. Water drops or oils can cause permanent stain or discoloration.
- E. To clean LCD module, please use IPA (Isopropyl Alcohol) or Hexane.
- F. Do not use ketone type material (ex. Acetone), ethyl alcohol, toluene, ethyl acid or methyl chloride. Using these could cause permanent polarizer damage to the LCD module.
- G. If the liquid crystal leaks from LCD module, keep it away from human eyes or mouth. In case of contact with human body or clothes, it should be washed with soap thoroughly.
- H. Protect LCD module from static discharge.
- To keep the LCD module clean, make sure to wear fabric gloves and finger coats when I. you are inspecting and/or assembling the unit.
- J. Do not disassemble LCD module.

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- K. Protection film on LCD module display area should be slowly peeled off just before assembly to prevent static discharge.
- L. Pins of the Interface connector should not be touched directly with bare hands.

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9.2 Storage Precautions

It is highly recommended to comply with the criteria in the table below

Item	Unit	Min.	Max.
Storage Temperature	(°C)	5	40
Storage Humidity	(%rH)	35	75
Storage life		12 months	
Storage Condition	Control - Products should not be from a wall - Prevent products from o Be cautious of a build u - Avoid other hazardous of - If products delivered or of 3 months, the recom	environment while storing kept in conditions of over mended temperature or h eave them at a temperatur	n the Pallet away or water; goods. the storage period umidity range,
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9.3 Operating Precautions

- A. If the module is used to other applications besides the recommendation on General Description, please contact SAMSUNG for application engineering device in advance
- B. Do not connect or disconnect the LCD module when it is set to the "Power On" condition.
- C. Input power should always follow '5.6 Power on/off sequence'
- D. Polarizer films are very fragile. It could be damaged easily. Do not press or scratch the Polarizer films
- E. LCD module contains electrical circuits that operate in high frequencies. To minimize electromagnetic interference, be sure to sufficiently ground and shield the LCD module and system.
- F. If LCD module containing system is out of SAMSUNG 's operating condition, SAMSUNG can not guarantee LCD module operating properly.
- G. If the product will be used in extreme conditions such as high temperature, humidity, display patterns, operation time, etc., it is strongly recommended to contact SAMSUNG for application engineering device. Otherwise, the reliability and function of the module may not be guaranteed. Extreme conditions are commonly found at airports, transit stations, banks, stocks, markets, and controlling systems.
- H. Ultra-violet ray filter is necessary for outdoor operation.
- I. If the module keeps displaying the same pattern for a long period of time, the image maybe burned in to the screen. To avoid image retention, it is recommended to use a screen saver.
- J. This module has its PCB's circuitry on the rear side and should be handled carefully in order to avoid stress.
- K. Please contact SAMSUNG beforehand, if you plan to display the same pattern for a long period of time.
- L. Any foreign materials brought into an LCD module by external forced-airflow are not guaranteed by SAMSUNG .

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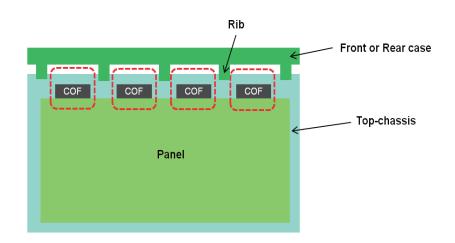
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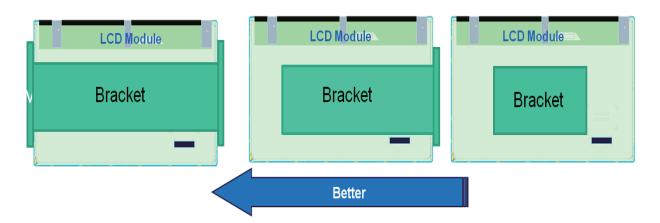
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9.4 Design Guide for System

- A. The LED driver should be designed in compliance with the specifications of LED bar strictly to make the LED in LCD module perform as expected
- B. It is recommended that you locate the rib on the front or rear cover not to be placed on the spot where D-IC is located on the upper or left of LCD module.

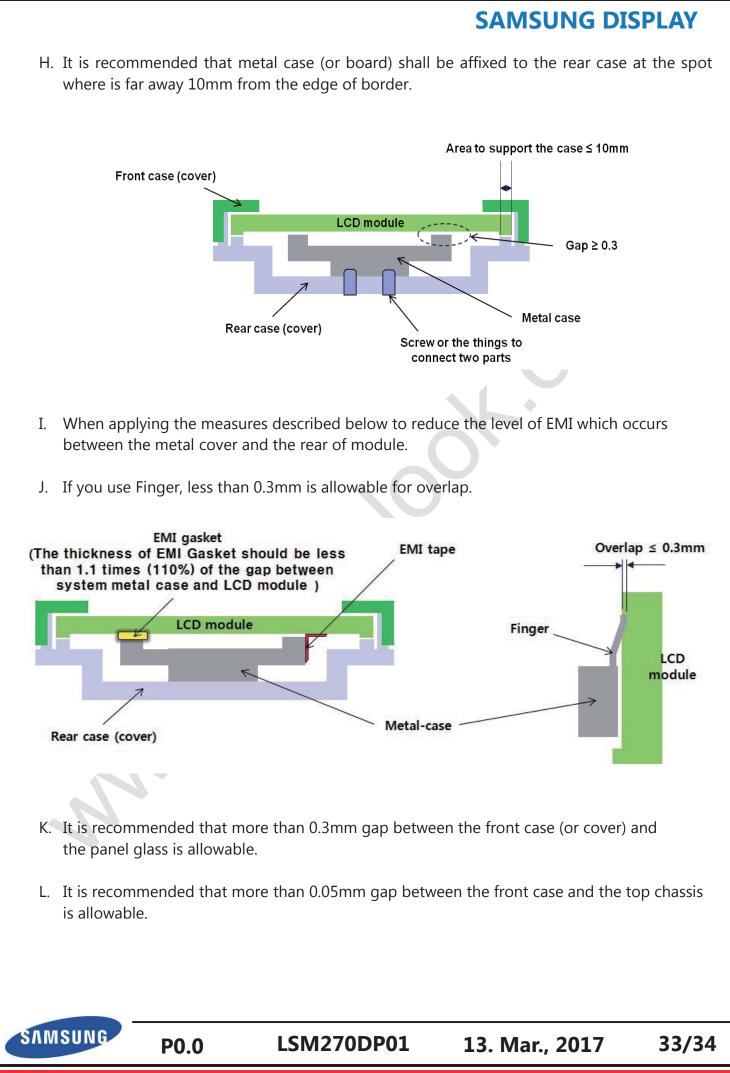


- C. It is recommended that assemble the bracket which has two sides with holes for assembly.
- D. It is recommended that you design the bracket with the structure which covers the sides of module when designing the bracket for customer.
- E. It is recommended that you design the bracket not to be interfered with the SET at the area where the PBA of module is located.

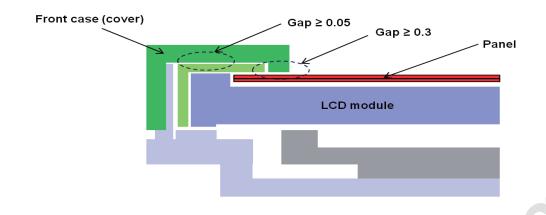


- F. It is recommended that more than 0.3 mm is allowable as a gap between the metal case and the rear of module.
- G. It is recommended that structure to support the module shall be far away 10mm from the edge of border.

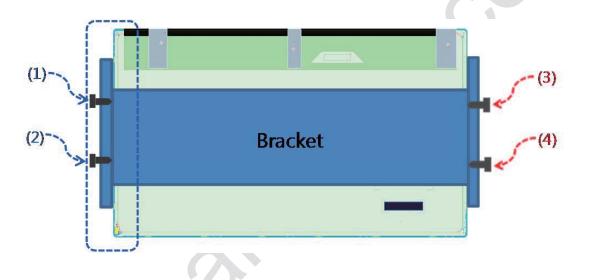
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M. It is recommended that insert the screws into user holes from the ones on the parts, which the light comes out to ones in the corresponding parts.



N. It is recommended that design the metal frame and the top chassis to be in parallel with having no gap after inserting the side screw.

